

## AMENDMENTS TO THE CLAIMS

This listing of claims will replace all prior versions, and listings, of claims in the application:

1. (Previously Presented) A speaker system, comprising:  
at least one transducer;  
at least one speaker analog circuit;  
a diagnostics circuit including a first test circuit and a second test circuit;  
the first test circuit being for analog diagnostics actuated in response to a  
diagnostic mode selection being made for generating one or more test  
signals for analog circuitry diagnosis and speaker diagnosis; and  
the second test circuit generating a signal to an AC power test indicator for  
indicating sufficient AC power being supplied to the speaker system and to  
an AC-to-DC conversion circuit for generating multiple DC voltages for  
providing analog diagnostic information indicating a sufficient supply of DC  
power for circuits in the speaker system.
2. (Original) The speaker system recited in Claim 1, wherein the diagnostics circuit  
further comprises a power diagnostics circuit.
3. (Original) The speaker system recited in Claim 2, wherein the power diagnostics  
circuit further comprises:  
a rectifier; and  
at least one AC power test indicator coupled to the rectifier.
4. (Cancelled).
5. (Cancelled).

6. (Currently Amended) The speaker system recited in Claim 5, ~~wherein the analog diagnostics circuit further comprises: 1, further comprising:~~  
an analog diagnostics circuit including a diagnostic mode activation mechanism.
7. (Cancelled).
8. (Currently Amended) The speaker system recited in Claim 1, ~~wherein the further comprising:~~  
an analog activity sensor further comprises comprising at least one transistor.
9. (Currently Amended) A speaker system recited in Claim 1, ~~wherein the further comprising:~~  
an analog activity sensor further comprises comprising at least one comparator.
10. (Currently Amended) A speaker system recited in Claim 6, wherein the analog diagnostics circuit includes a diagnostic signal generation circuit and each at least one transducer is coupled to the diagnostic signal generation circuit.
11. (Cancelled).
12. (Previously Presented) A computer system comprising:  
a processor;  
a memory coupled to the processor;  
a speaker system coupled to the processor, wherein the speaker system

includes a diagnostics circuit including a first test circuit and a second test circuit;

the first test circuit being for analog diagnostics actuated in response to a diagnostic mode selection being made for generating one or more test signals for analog circuitry diagnosis and speaker diagnosis;

the second test circuit generating a signal to an AC power test indicator for indicating sufficient AC power being supplied to the speaker system and to an AC-to-DC conversion circuit for generating multiple DC voltages for providing analog diagnostic information indicating a sufficient supply of DC power for circuits in the speaker system; and

at least one transducer.

13. (Cancelled).
14. (Original) The computer system recited in Claim 12, wherein the diagnostics circuit further comprises a power diagnostics circuit.
15. (Original) The computer system recited in Claim 14, wherein the power diagnostics circuit further comprises:  
a rectifier; and  
at least one AC power test indicator coupled to the rectifier.
16. (Cancelled).
17. (Cancelled).

18. (Currently Amended) The computer system recited in Claim ~~17~~, wherein the ~~analog diagnostics circuit further comprises~~: 12, further comprising:  
an analog diagnostics circuit including a diagnostic mode activation mechanism.
19. (Cancelled).
20. (Currently Amended) The computer system recited in Claim 12, wherein the ~~further comprising~~:  
an analog activity sensor further comprises comprising at least one transistor.
21. (Currently Amended) The computer system recited in Claim 12, wherein the ~~further comprising~~:  
an analog activity sensor further comprises comprising at least one comparator.
22. (Currently Amended) The computer system recited in Claim 18, wherein the analog diagnostics circuit includes a diagnostic signal generation circuit and each at least one transducer is coupled to the diagnostic signal generation circuit.
23. (Cancelled).